

(FILE 'USPAT' ENTERED AT 19:12:18 ON 19 JUN 1998)

L1	45 S DISK AND POWER CALIBRATION
L2	0 S L1 AND WRITE PROTECTION
L3	5 S L1 AND RING
L4	0 S L3 AND DATA AREA
L5	219 S DISK AND WRITE PROTECTION
L6	0 S L5 AND POWER CALIBRATION
L7	152 S L5 AND POWER
L8	39 S L7 AND RING
L9	16 S L8 AND CALIBRA?
L10	16 S L9 AND DATA
L11	9 S L10 AND LASER
L12	1 S L11 AND INDENTED
L13	73 S DISC AND WRITE PROTECTION
L14	1 S L13 AND POWER CALIBRATION
L15	25 S L13 AND LASER
L16	18 S L15 AND POWER
L17	17 S L16 AND DATA
L18	8 S L17 AND RING
L19	1 S L18 AND ADHESIVE

=> d 114

1. 5,546,365, Aug. 13, 1996, Record carriers containing a user's preferred order of reproduction of items of information included therein, and apparatus for use with and/or for creating such record carriers; Rudolf Roth, 369/32, 47, 54, 58, 275.3 [IMAGE AVAILABLE]

=> d 119

1. 5,015,106, May 14, 1991, Marking apparatus with multiple line capability; John A. Robertson, et al., 400/279; 137/883; 400/127 [IMAGE AVAILABLE]

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L1 45 S DISK AND POWER CALIBRATION
L2 0 S L1 AND WRITE PROTECTION
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L5 219 S DISK AND WRITE PROTECTION
L6 0 S L5 AND POWER CALIBRATION
L7 152 S L5 AND POWER
L8 39 S L7 AND RING
L9 16 S L8 AND CALIBRA?
L10 16 S L9 AND DATA
L11 9 S L10 AND LASER
L12 1 S L11 AND INDENTED

=> d 111 1-9

1. 5,754,837, May 19, 1998, Clock control circuits, systems and methods; James J. Walsh, et al., 395/555, 558, 559, 560 [IMAGE AVAILABLE]

2. 5,754,436, May 19, 1998, Adaptive **power** management processes, circuits and systems; James J. Walsh, et al., 395/838

3. 5,734,919, Mar. 31, 1998, Systems, circuits and methods for mixed voltages and programmable voltage rails on integrated circuits; James J. Walsh, et al., 365/63, 185.13 [IMAGE AVAILABLE]

4. 5,729,720, Mar. 17, 1998, **Power** management masked clock circuitry, systems and methods; Weiyuen Kau, et al., 395/555, 556 [IMAGE AVAILABLE]

5. 5,727,221, Mar. 10, 1998, Computer system **power** management interconnection circuitry and systems; James J. Walsh, et al., 395/750.02; 364/491, 707; 395/733, 750.05 [IMAGE AVAILABLE]

6. 5,721,933, Feb. 24, 1998, **Power** management supply interface circuitry, systems and methods; James J. Walsh, et al., 395/750.01; 307/31, 66 [IMAGE AVAILABLE]

7. 5,710,911, Jan. 20, 1998, Clock control circuits, systems and methods; James J. Walsh, et al., 395/555, 558, 559, 560, 750.04 [IMAGE AVAILABLE]

8. 5,684,997, Nov. 4, 1997, Integrated circuit design for handling of system management interrupts (SMI); Weiyuen Kau, et al., 395/733, 309, 734 [IMAGE AVAILABLE]

9. 5,015,106, May 14, 1991, Marking apparatus with multiple line capability; John A. Robertson, et al., 400/279; 137/883; 400/127 [IMAGE AVAILABLE]

=> d 112

1. 5,015,106, May 14, 1991, Marking apparatus with multiple line capability; John A. Robertson, et al., 400/279; 137/883; 400/127 [IMAGE AVAILABLE]

=> d 13 1-5

1. 5,748,578, May 5, 1998, Colpitts type oscillator having reduced ringing and improved optical disc system utilizing same; David L. Schell, 369/44.14; 331/75, 117R; 369/121 [IMAGE AVAILABLE]
2. 5,729,511, Mar. 17, 1998, Optical disc system having servo motor and servo error detection assembly operated relative to monitored quad sum signal; David L. Schell, et al., 369/44.27, 13, 44.29, 116 [IMAGE AVAILABLE]
3. 5,708,633, Jan. 13, 1998, Method and apparatus for manufacturing information storage devices; Zdenek A. Hollen, et al., 369/44.11; 359/813; 369/44.14 [IMAGE AVAILABLE]
4. 5,677,899, Oct. 14, 1997, Method for moving carriage assembly from initial position to target position relative to storage medium; Kurt W. Getreuer, 369/44.28 [IMAGE AVAILABLE]
5. 5,607,739, Mar. 4, 1997, Temperature sensor and method for optical disk; Brian J. Bartholomeusz, 428/64.1; 369/275.1, 283, 288; 428/64.2, 64.4, 64.8, 913; 430/270.1, 270.11, 270.14, 495.1, 945 [IMAGE AVAILABLE]